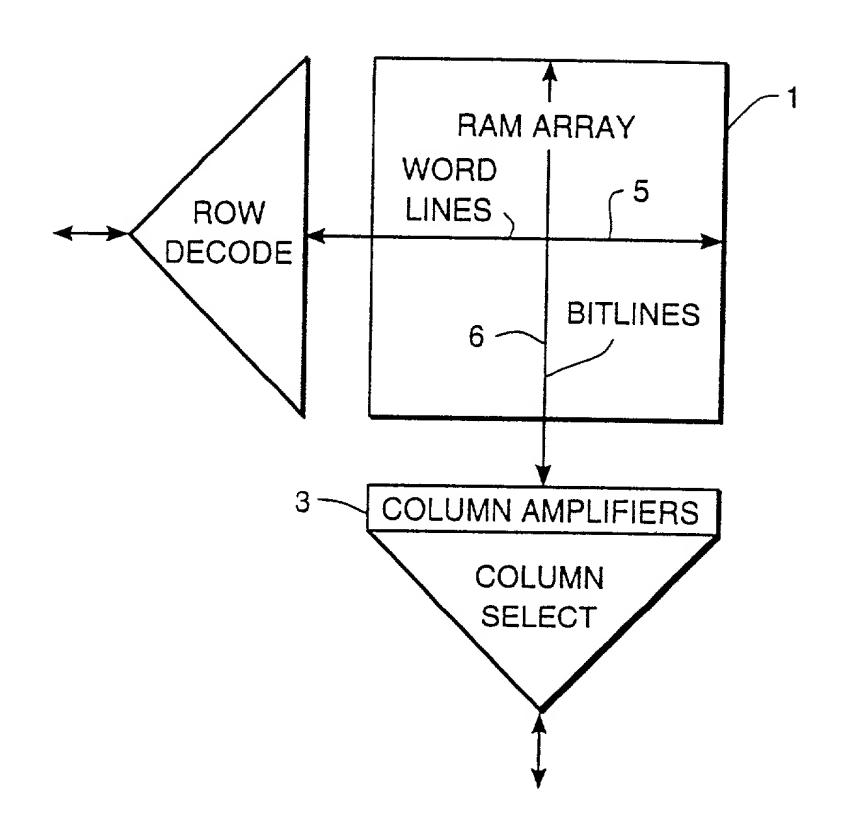
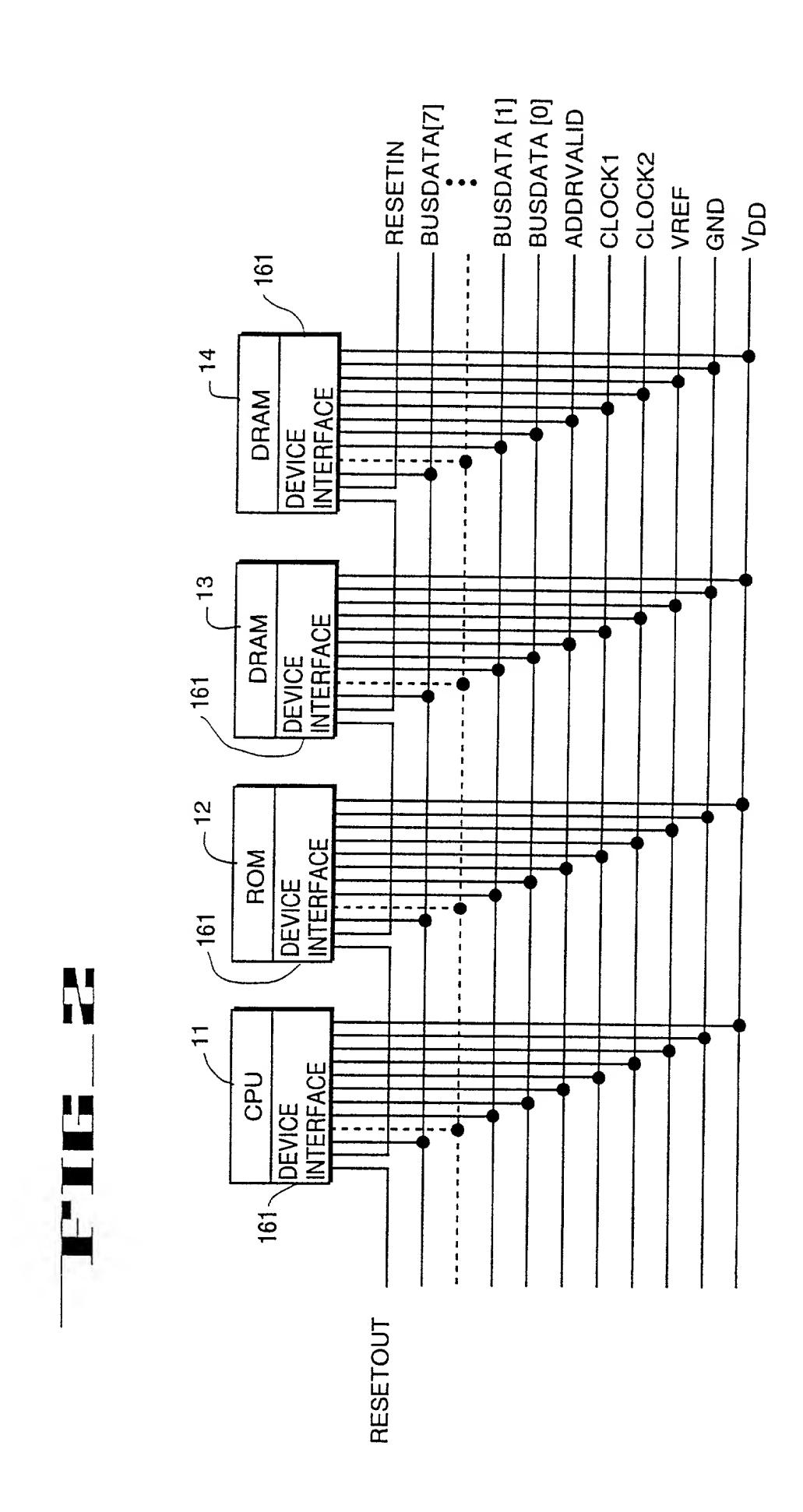
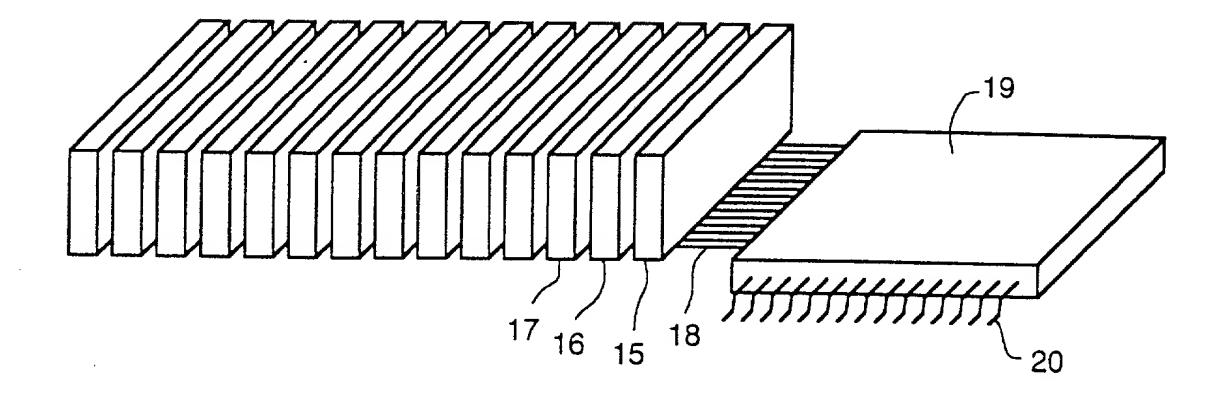
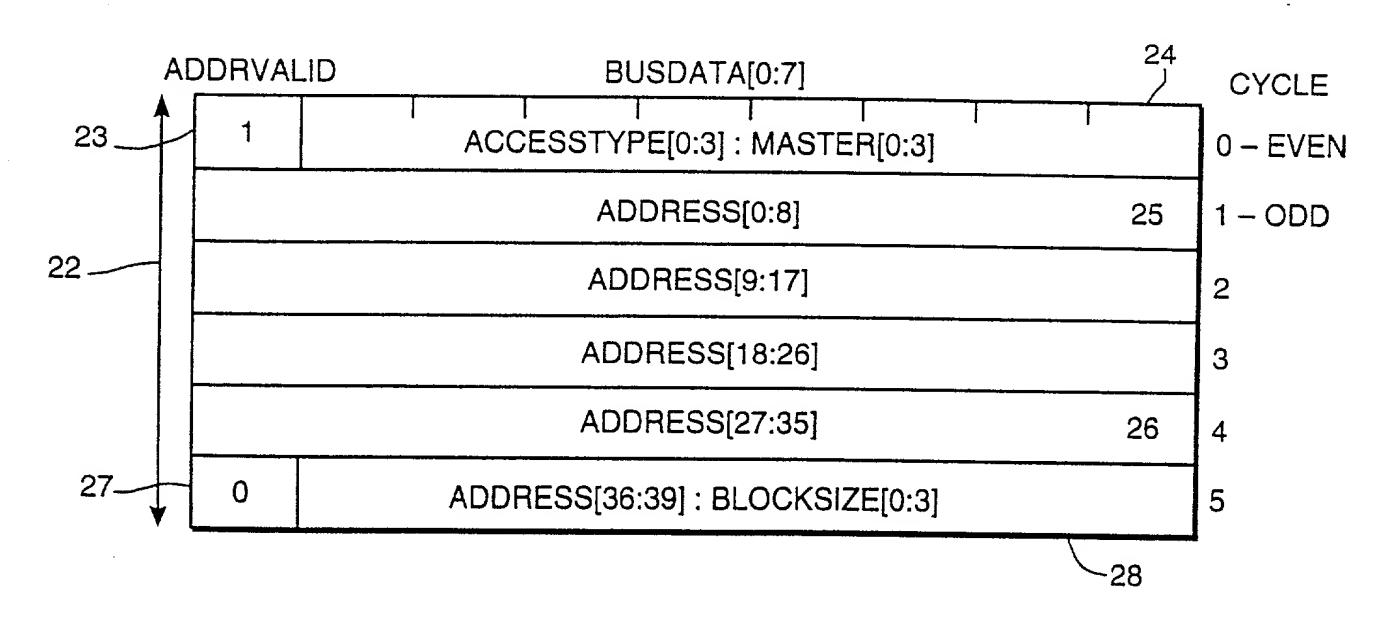
FIELL







REGULAR ACCESS

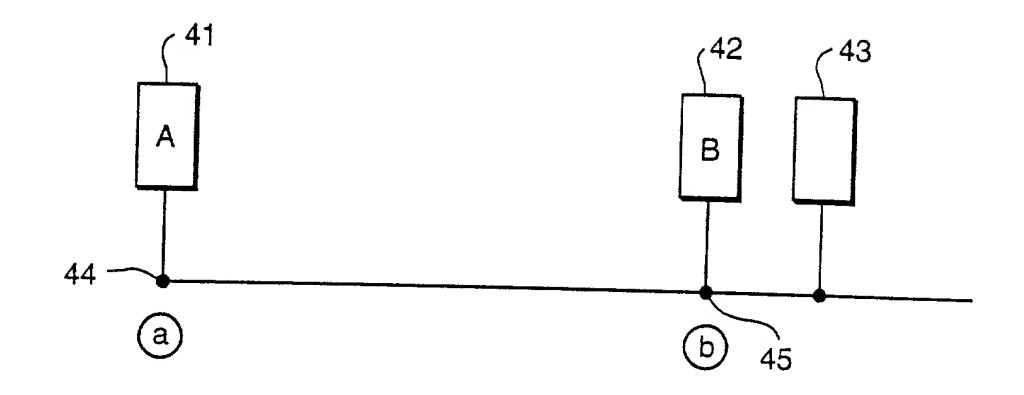


F16_4

ADDRVALID		BUSDATA[0:7]	CYCLE
23	1	ACCESSTYPE[0:3] : MASTER[0:3] = 0	0 - EVEN
	0	INFO[0:7]	1 – ODD

Αl	DDRVALID	BUSDATA[0:7]		CYCLE
23	1	ACCESSTYPE[0:3] : MASTER[0:3]	24	0
22-		ADDRESS[0:8]	25	1
		ADDRESS[9:17]		2
		ADDRESS[18:26]		3
		ADDRESS[27:35]	26	4
27	1	ADDRESS[36:39] : BLOCKSIZE[0:3]	28	5
		XXX	29	6
	0	XXX	30	7
		XXX	31	8
29	0	XXX	32	9
	0	INVALID: REQUEST[1:7]	33	10
	0	REQUEST[8:15]	34	11





FIG_ZA

